AC/DC Drivers

AC/DC controller IC for LED lighting

BD521GOFJ

General Description
BD521GOFJ is an AC/DC converter for LED lighting. This IC can be applied to quasi-resonant typed high-side LED driver application, and the high current precision and low EMI noise can be achieved. Moreover, owing to the built-in PFC (Power Factor Correction) converter, the harmonic is improved.

By choosing the external MOSFET, the LED drivers for from low power to high power can be achieved. So that, it can be widely used on from the low power lighting such as spotlight to the high power lighting such as base light.

Owing to the external current setting resistance, a power supply design with a high degree of freedom can be achieved.

Key Specifications
- Operating Power Supply Voltage Range: VCC 8.9V to 25.0V
- Operating Current: Normal Operation: 400μA (Typ)
- Operating Temperature Range: -40°C to +105°C

Features
- Quasi-resonant Switching Mode + PFC Circuit
- Non-isolated Application
- Maximum Frequency 300kHz
- VCC Pin: Under Voltage Protection
- VCC Pin: Over Voltage Protection (Latch)
- CS Pin: Leading-Edge-Blanking Function
- CS Pin: OPEN Protection Function
- LED Over Current Detection Function

Package
SOP-J8

W(Typ) x D(Typ) x H(Max)
4.90mm x 6.00mm x 1.65mm

Application
LED Bulb, Down-light, Tube Light etc.
Electrical Machineries for LED Lighting

Typical Application Circuit

Figure 1. Application Circuit
Absolute Maximum Ratings (Ta=25°C)

<table>
<thead>
<tr>
<th>Item</th>
<th>Symbol</th>
<th>Rating</th>
<th>Unit</th>
<th>Condition</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Voltage Range 1</td>
<td>Vmax1</td>
<td>-0.3 to 30</td>
<td>V</td>
<td>VCC, CHG</td>
</tr>
<tr>
<td>Input Voltage Range 2</td>
<td>Vmax2</td>
<td>-0.3 to 6.5</td>
<td>V</td>
<td>CS, COMP, LPF, ZT</td>
</tr>
<tr>
<td>Input Voltage Range 3</td>
<td>Vmax3</td>
<td>-0.3 to 15.5</td>
<td>V</td>
<td>OUT</td>
</tr>
<tr>
<td>Operating Temperature Range</td>
<td>Topr</td>
<td>-40 to +105</td>
<td>°C</td>
<td></td>
</tr>
<tr>
<td>Maximum Junction Temperature</td>
<td>Tjmax</td>
<td>150</td>
<td>°C</td>
<td></td>
</tr>
<tr>
<td>Storage Temperature Range</td>
<td>Tstg</td>
<td>-55 to +150</td>
<td>°C</td>
<td></td>
</tr>
</tbody>
</table>

Caution: Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.

Thermal Resistance (Note 1)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Thermal Resistance (Typ)</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>1s (Note 3)</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>2s2p (Note 4)</td>
<td></td>
</tr>
<tr>
<td>SOP-J8</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Junction to Ambient</td>
<td>θJA</td>
<td>149.3</td>
<td>76.9</td>
</tr>
<tr>
<td>Junction to Top Characterization Parameter (Note 2)</td>
<td>ΨJT</td>
<td>18</td>
<td>11</td>
</tr>
</tbody>
</table>

(Note 1) Based on JESD51-2A (Still-Air)
(Note 2) The thermal characterization parameter to report the difference between junction temperature and the temperature at the top center of the outside surface of the component package.
(Note 3) Using a PCB board based on JESD51-3.
(Note 4) Using a PCB board based on JESD51-7.

Layer Number of Measurement Board | Material | Board Size |
<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Single</td>
<td>FR-4</td>
<td>114.3mm x 76.2mm x 1.57mmt</td>
</tr>
<tr>
<td>Top Copper Pattern Thickness</td>
<td>70μm</td>
<td></td>
</tr>
<tr>
<td>Footprints and Traces</td>
<td>70μm</td>
<td></td>
</tr>
</tbody>
</table>

Layer Number of Measurement Board | Material | Board Size |
<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>4 Layers</td>
<td>FR-4</td>
<td>114.3mm x 76.2mm x 1.6mmt</td>
</tr>
<tr>
<td>Top 2 Internal Layers Bottom</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Copper Pattern Thickness</td>
<td>70μm</td>
<td></td>
</tr>
<tr>
<td>Copper Pattern Thickness</td>
<td>74.2mm x 74.2mm</td>
<td></td>
</tr>
<tr>
<td>Copper Pattern Thickness</td>
<td>35μm</td>
<td></td>
</tr>
<tr>
<td>Copper Pattern Thickness</td>
<td>74.2mm x 74.2mm</td>
<td></td>
</tr>
<tr>
<td>Copper Pattern Thickness</td>
<td>70μm</td>
<td></td>
</tr>
</tbody>
</table>

Recommended Operating Condition (Ta=25°C)

<table>
<thead>
<tr>
<th>Item</th>
<th>Symbol</th>
<th>Range</th>
<th>Unit</th>
<th>Condition</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Voltage Range</td>
<td>Vcc</td>
<td>8.9 to 25.0</td>
<td>V</td>
<td>VCC Voltage</td>
</tr>
</tbody>
</table>
### Electrical Characteristics (Unless otherwise specified VCC=15 V Ta = 25°C)

<table>
<thead>
<tr>
<th>Item</th>
<th>Symbol</th>
<th>Specification</th>
<th>Unit</th>
<th>Condition</th>
</tr>
</thead>
<tbody>
<tr>
<td>[ Circuit Current ]</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Circuit Current (ON)</td>
<td>I&lt;sub&gt;ON&lt;/sub&gt;</td>
<td>-</td>
<td>400</td>
<td>800</td>
</tr>
<tr>
<td>Circuit Current (OFF)</td>
<td>I&lt;sub&gt;OFF&lt;/sub&gt;</td>
<td>-</td>
<td>300</td>
<td>600</td>
</tr>
<tr>
<td>[ CHG Pin Charge Circuit ]</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CHG Charge Current 1</td>
<td>I&lt;sub&gt;START1&lt;/sub&gt;</td>
<td>0.25</td>
<td>0.35</td>
<td>0.45</td>
</tr>
<tr>
<td>CHG Charge Current 2</td>
<td>I&lt;sub&gt;START2&lt;/sub&gt;</td>
<td>2</td>
<td>6</td>
<td>12</td>
</tr>
<tr>
<td>CHG OFF Current</td>
<td>I&lt;sub&gt;START3&lt;/sub&gt;</td>
<td>3</td>
<td>6</td>
<td>9</td>
</tr>
<tr>
<td>CHG Charge Current Switching Voltage</td>
<td>V&lt;sub&gt;SC&lt;/sub&gt;</td>
<td>0.4</td>
<td>0.8</td>
<td>1.6</td>
</tr>
<tr>
<td>[ VCC Pin Protection ]</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>VCC UVLO Voltage 1</td>
<td>V&lt;sub&gt;UVLO1&lt;/sub&gt;</td>
<td>12.5</td>
<td>13.5</td>
<td>14.5</td>
</tr>
<tr>
<td>VCC UVLO Voltage 2</td>
<td>V&lt;sub&gt;UVLO2&lt;/sub&gt;</td>
<td>7.5</td>
<td>8.2</td>
<td>8.9</td>
</tr>
<tr>
<td>VCC UVLO Hysteresis</td>
<td>V&lt;sub&gt;UVLO3&lt;/sub&gt;</td>
<td>-</td>
<td>5.3</td>
<td>-</td>
</tr>
<tr>
<td>VCC OVP Trigger Voltage</td>
<td>V&lt;sub&gt;OVP1&lt;/sub&gt;</td>
<td>25.0</td>
<td>27.5</td>
<td>30.0</td>
</tr>
<tr>
<td>VCC OVP Release Voltage</td>
<td>V&lt;sub&gt;OVP2&lt;/sub&gt;</td>
<td>21.0</td>
<td>23.5</td>
<td>26.0</td>
</tr>
<tr>
<td>Latch Released VCC Voltage</td>
<td>V&lt;sub&gt;LATCH&lt;/sub&gt;</td>
<td>-</td>
<td>V&lt;sub&gt;UVLO2&lt;/sub&gt; - 0.5</td>
<td>-</td>
</tr>
<tr>
<td>VCC Recharge Start Voltage</td>
<td>V&lt;sub&gt;CHG1&lt;/sub&gt;</td>
<td>7.7</td>
<td>8.7</td>
<td>9.7</td>
</tr>
<tr>
<td>VCC Recharge End Voltage</td>
<td>V&lt;sub&gt;CHG2&lt;/sub&gt;</td>
<td>12</td>
<td>13</td>
<td>14</td>
</tr>
<tr>
<td>Latch Mask Time</td>
<td>T&lt;sub&gt;LATCH&lt;/sub&gt;</td>
<td>75</td>
<td>150</td>
<td>300</td>
</tr>
<tr>
<td>[ DC/DC Converter Block (Turn-on) ]</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ZT Comparator Voltage 1</td>
<td>V&lt;sub&gt;ZT1&lt;/sub&gt;</td>
<td>30</td>
<td>110</td>
<td>190</td>
</tr>
<tr>
<td>ZT Comparator Voltage 2</td>
<td>V&lt;sub&gt;ZT2&lt;/sub&gt;</td>
<td>100</td>
<td>200</td>
<td>300</td>
</tr>
<tr>
<td>ZT Comparator Hysteresis</td>
<td>V&lt;sub&gt;ZTHYS&lt;/sub&gt;</td>
<td>40</td>
<td>90</td>
<td>140</td>
</tr>
<tr>
<td>ZT Trigger Timeout</td>
<td>T&lt;sub&gt;ZTOUT&lt;/sub&gt;</td>
<td>9</td>
<td>18</td>
<td>36</td>
</tr>
<tr>
<td>[ DC/DC Converter Block (Turn-off) ]</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Current Trigger Voltage</td>
<td>V&lt;sub&gt;CS&lt;/sub&gt;</td>
<td>2.60</td>
<td>2.95</td>
<td>3.30</td>
</tr>
<tr>
<td>Maximum Frequency</td>
<td>F&lt;sub&gt;SW&lt;/sub&gt;</td>
<td>255</td>
<td>300</td>
<td>345</td>
</tr>
<tr>
<td>Leading Edge Blank Time</td>
<td>T&lt;sub&gt;LEB&lt;/sub&gt;</td>
<td>0.1</td>
<td>0.2</td>
<td>0.4</td>
</tr>
<tr>
<td>Error Amplifier Reference Voltage</td>
<td>V&lt;sub&gt;REF&lt;/sub&gt;</td>
<td>0.582</td>
<td>0.600</td>
<td>0.618</td>
</tr>
<tr>
<td>[ Driver Block ]</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>OUT Pin High Voltage</td>
<td>V&lt;sub&gt;OUTH&lt;/sub&gt;</td>
<td>10.5</td>
<td>12.0</td>
<td>14.5</td>
</tr>
<tr>
<td>OUT High ON Resistance</td>
<td>R&lt;sub&gt;OUTH&lt;/sub&gt;</td>
<td>23</td>
<td>45</td>
<td>76</td>
</tr>
<tr>
<td>OUT Low ON Resistance</td>
<td>R&lt;sub&gt;OUTL&lt;/sub&gt;</td>
<td>33</td>
<td>65</td>
<td>110</td>
</tr>
</tbody>
</table>
Pin Configuration

![Diagram of pin configuration](image)

**Table 1. PIN Functions**

<table>
<thead>
<tr>
<th>NO.</th>
<th>Pin Name</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>ZT</td>
<td>Zero current detecting pin</td>
</tr>
<tr>
<td>2</td>
<td>VCC</td>
<td>Power supply pin</td>
</tr>
<tr>
<td>3</td>
<td>CHG</td>
<td>Input pin of the charge circuit</td>
</tr>
<tr>
<td>4</td>
<td>OUT</td>
<td>GATE pin of the external MOSFET</td>
</tr>
<tr>
<td>5</td>
<td>CS</td>
<td>Inductor current sensing pin</td>
</tr>
<tr>
<td>6</td>
<td>COMP</td>
<td>Error amplifier output pin</td>
</tr>
<tr>
<td>7</td>
<td>GND</td>
<td>GND pin</td>
</tr>
<tr>
<td>8</td>
<td>LPF</td>
<td>Error amplifier input pin</td>
</tr>
</tbody>
</table>

I/O Equivalence Circuits

![Diagram of I/O equivalence circuits](image)
Block Diagram

Figure 4. Block Diagram
Description of Blocks

(1) Charge Circuit Block (VCC: 2pin, CHG: 3pin)

(1.1) Block Description
A charge circuit is built in this IC. After the IC starts up, the power consumption becomes only the idling current $I_{START3}$ (Typ=6μA).

For reducing the number of the components, the application schematic which consists by the starter resistor $RSTR$ and the protection zener diode $DZ3$ is showed in Figure7.

Figure 5. Charge Circuit Block Diagram
Figure 6. Charge Current—VCC Voltage Curve

Figure 7. Application Circuit Example for DCDC (The internal charge circuit is not used)
(1.2) Start Sequences

The time chart of the start sequences are showed in Figure 8.

A: Input voltage is applied to CHG pin when the AC power supply turns ON.
B: The capacitor connected to the VCC pin is charged by the start-up current from the CHG pin, and the VCC rises up.
C: The IC starts operating when VCC > VUVLO1 (Typ=13.5V)
D: By the increase of the COMP pin voltage, the IC starts up with the soft-start operation.
E: The current is supplied to VCC pin from LED+ and D2 by the switching operation of OUT pin.
   ※ The power is supplied from LED+, and the VCC voltage is determined by the VDZ1 (the breakdown voltage of DZ1).
   \[ V_{CC} = V_{LED} - V_{DZ1} - V_fD2 \]
   \( (V_{LED}: \text{the Vf of LED}, \ V_{DZ1}: \text{the breakdown voltage of DZ1}, \ V_fD2: \text{the Vf of D2} ) \)
F: When the power supply turns OFF, VCC voltage falls down due to the drop of the power supply.
   The IC turns OFF when the VUVLO2 (Typ=8.2V) is triggered.
(1.3) VCC Pin Protection Functions
The VCC under voltage protection function VCC UVLO (Under Voltage Lock Out), over voltage protection function VCC OVP (Over Voltage Protection), and a VCC recharge function which operates when a voltage drop occurs at VCC pin are built in this IC. The VCC UVLO and VCC OVP functions are used for preventing from the destructions of the switching MOSFET which occurs when the VCC voltage gets too high or too low.

Owing to the VCC charge function, the VCC pin is charged by the charge circuit when the VCC voltage drops.

VCC UVLO / VCC OVP Function
VCC UVLO and VCC OVP are comparators which have voltage hysteresis. (The comparator of VCC UVLO is auto recovery type, and the comparator of VCC OVP is latch type.)
VCC OVP has a built-in mask time $T_{LATCH}$ (Typ=150 $\mu$s).
When the VCC voltage is over $V_{OVP1}$ (Typ=27.5V), and this state lasts $T_{LATCH}$, the detection is executed.
Owing to this function, the surge which occurs at VCC pin can be masked.

VCC Charge Function
When the VCC pin voltage is over $V_{UVLO1}$ (Typ=13.5V), the IC starts up. In this case, if the VCC pin voltage drops below $V_{CHG1}$ (Typ=8.7V), VCC charge function operates. At this time, the VCC pin is charged from the CHG pin through the charge circuit. Owing to this operation, the failure of start-up can be prevented.

Figure 9. Timing Chart of VCC UVLO/ VCC OVP / VCC Charge Function
A: CHG pin voltage is applied, VCC voltage rises by the charging current $I_{start1}$ (Typ=350 $\mu$A).
B: VCC voltage> $V_{SC}$ (Typ=0.8V) the charging current to VCC changes from $I_{start1}$ to $I_{start2}$ (Typ=6mA).
C: VCC voltage> $V_{CHG2}$ (Typ=13V) though VCC charge function reacts, due to VCC UVLO is detected, the charge continues.
D: VCC voltage> $V_{UVLO1}$, the VCC UVLO is released, the DC/DC operation starts, and the VCC charge operation stops.
E: VCC voltage< $V_{CHG1}$, the VCC charge operation restarts.
F: VCC voltage> $V_{OVP2}$ (Typ=23.5V), if VCC voltage drops below $V_{OVP2}$ in $T_{LATCH}$, VCC OVP is released, and the latch will not be activated.
I: VCC voltage > $V_{OVP2}$ if this state is kept longer than $T_{LATCH}$, the switching stops by latch.
J: VCC voltage< $V_{UVLO2}$ (Typ=8.2V), VCC UVLO is detected.
K: VCC voltage< $V_{LATCH}$ (Typ=7.7V), the latch state is released.
L: VCC voltage< $V_{SC}$, the charge current to VCC changes from $I_{start2}$ to $I_{start1}$.
(2) Error Amplifier Block (CS: 5pin, COMP: 6 pin, LPF: 8pin)

(2.1) Block Description
An error amplifier is built in this IC, the High time of the OUT pin changes by controlling the output of the error amplifier. The voltage which occurs on the current sensing resistor RS (CS Pin Voltage) is inputted to the RC filter circuit which is composed by R4, C2. The output of the low pass filter circuit (LPF Pin Voltage) is used as the input voltage of the error amplifier, and the summation of the current which flows through the inductor L2 while the MOSFET is ON, and the current which flows through the inductor L2 from the flywheel diode D1 while the MOSFET is OFF flows through the RS.

Figure 10. Error Amplifier Block Diagram

(2.2) CS Pin OCP Protection
An OCP (Over Current Protection) function is built in the CS pin. While the voltage of CS pin rises up to the trigger voltage \( V_{OCP} \) (Typ=2.95V), the MOSFET is forced to turn OFF. (Automatic Recovery Protection)

(2.3) L.E.B Blanking Period
When the MOSFET is turned ON, surge current occurs by the capacitive components and drive current. In this case, if the CS pin voltage rises temporarily, the false detections may occur in the over current limiter circuit. For preventing from the false detections, an L.E.B (Leading Edge Blanking) function which masks the CS voltage during a constant time \( T_{LEB} \) (Typ=200ns) after the OUT pin switches from Low to High is built in.

(2.4) CS Pin Open Protection
When the CS pin becomes open, this function stops the switching operation for protection. (Automatic Recovery Protection)
(3) Zero current detection block (ZT : 1pin)

(3.1) Block Description

ZT pin (1pin) controls the Low time of the output of OUT pin. While the output is Low (MOSFET: OFF), the current flows from the flywheel diode D1 to the inductor L2. While the current I_L is close to zero, the voltage of ZT pin falls down. While the voltage becomes lower than V_ZT1 (Typ=110mV), it is detected by the ZT comparator, and the output of OUT pin turns to High (MOSFET: ON).

![Figure 11. Zero-cross Detection by ZT Pin](image)

(3.2) ZT Trigger Timeout

In case of the ZT pin is locked at low voltage which is caused by some malfunctions of the components on the application, and the quasi-resonant switching operation stops, the output of OUT pin turns to High after T_ZTOUT (Typ=18 μs).

![Figure 12. ZT Trigger Timeout Operation](image)

(A)…When the ON time (T_OUT_ON) which is determined by the voltage of COMP pin passed, the output of OUT pin turns to Low.
(B)…The ZT pin is locked at a low voltage, the output of OUT pin keeps Low.
(C)…When T_ZTOUT passed after the output of OUT pin turns to Low, the output of OUT pin turns to High.

(3.3) Maximum Frequency

The maximum frequency is a function which is designed for preventing from the overheating of the IC, and it limits the IC to operate under the F_MAX (Typ=300kHz).

![Figure 13. Maximum Frequency Operation](image)

(A)…When the ON time (T_OUT_ON) which is determined by the voltage of COMP pin passed, the output of OUT pin turns to Low.
(B)…If the switching frequency exceeds the F_MAX, the output of OUT pin cannot turn to High.
(C)…When T_ZTOUT passed after the output of OUT pin turns to Low, the output of OUT pin is forced to turn to High.
## Operation Mode of Protection Circuit

Operation mode of protection functions are shown in Table 2.

<table>
<thead>
<tr>
<th>Abnormal State Detection</th>
<th>Detection</th>
<th>Release</th>
<th>Protection Operations</th>
</tr>
</thead>
<tbody>
<tr>
<td>VCC</td>
<td>UVLO &lt;= 8.2V(Typ)</td>
<td>&gt;= 13.5V (Typ)</td>
<td>IC Stops (Automatic Recovery)</td>
</tr>
<tr>
<td></td>
<td>OVP &gt;=27.5V(Typ)</td>
<td>Before Latch: &lt;= 23.5V (Typ)</td>
<td>150μs(Typ) Timer Latch</td>
</tr>
<tr>
<td></td>
<td>CS &gt;=2.95V(Typ)</td>
<td>&lt;2.95V (Typ)</td>
<td>Output Stops (Automatic Recovery)</td>
</tr>
<tr>
<td></td>
<td>TSD &gt;=175°C (Typ)</td>
<td>Before Latch: &lt;= 155°C (Typ)</td>
<td>150μs(Typ) Timer Latch</td>
</tr>
</tbody>
</table>
Operational Notes

1. **Reverse Connection of Power Supply**
   Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC’s power supply pins.

2. **Power Supply Lines**
   Design the PCB layout pattern to provide low impedance supply lines. Separate the ground and supply lines of the digital and analog blocks to prevent noise in the ground and supply lines of the digital block from affecting the analog block. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

3. **Ground Voltage**
   Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

4. **Ground Wiring Pattern**
   When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

5. **Thermal Consideration**
   Should by any chance the maximum junction temperature rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. In case of exceeding this absolute maximum rating, increase the board size and copper area to prevent exceeding the maximum junction temperature rating.

6. **Recommended Operating Conditions**
   These conditions represent a range within which the expected characteristics of the IC can be approximately obtained. The electrical characteristics are guaranteed under the conditions of each parameter.

7. **Inrush Current**
   When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

8. **Operation Under Strong Electromagnetic Field**
   Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

9. **Testing on Application Boards**
   When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC’s power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

10. **Inter-pin Short and Mounting Errors**
    Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.
11. Unused Input Terminals
Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.

12. Regarding the Input Pin of the IC
This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of the P layers with the N layers of other elements, creating a parasitic diode or transistor. For example (refer to figure below):

When GND > Pin A and GND > Pin B, the P-N junction operates as a parasitic diode.
When GND > Pin B, the P-N junction operates as a parasitic transistor.

Parasitic diodes inevitably occur in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions that cause these diodes to operate, such as applying a voltage lower than the GND voltage to an input pin (and thus to the P substrate) should be avoided.

13. Ceramic Capacitor
When using a ceramic capacitor, determine the dielectric constant considering the change of capacitance with temperature and the decrease in nominal capacitance due to DC bias and others.

14. Area of Safe Operation (ASO)
Operate the IC such that the output voltage, output current, and the maximum junction temperature rating are all within the Area of Safe Operation (ASO).

15. Thermal Shutdown Circuit(TSD)
This IC has a built-in thermal shutdown circuit that prevents heat damage to the IC. Normal operation should always be within the IC’s maximum junction temperature rating. If however the rating is exceeded for a continued period, the junction temperature (Tj) will rise which will activate the TSD circuit that will turn OFF all output pins. The IC should be powered down and turned ON again to resume normal operation because the TSD circuit keeps the outputs at the OFF state even if the TJ falls below the TSD threshold.
Note that the TSD circuit operates in a situation that exceeds the absolute maximum ratings and therefore, under no circumstances, should the TSD circuit be used in a set design or for any purpose other than protecting the IC from heat damage.

16. Over Current Protection Circuit (OCP)
This IC incorporates an integrated overcurrent protection circuit that is activated when the load is shorted. This protection circuit is effective in preventing damage due to sudden and unexpected incidents. However, the IC should not be used in applications characterized by continuous operation or transitioning of the protection circuit.
Ordering Information

B D 5 2 1 G O F J - XX

Product name  Package  Packaging and forming specification
FJ: SOP-J8

XX: Please confirm the formal name with our salesmen.

Marking Diagram

SOP-J8(TOP VIEW)

- Part Number Marking
- LOT Number
- 1PIN MARK

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Physical Dimension, Tape and Reel Information

<table>
<thead>
<tr>
<th>Package Name</th>
<th>SOP-J8</th>
</tr>
</thead>
</table>

**Diagram:**

- **Max:** 5.25 (include BURR)
- **Dimensions:**
  - 4.9 ± 0.2
  - 3.9 ± 0.2
  - 6.0 ± 0.3
  - 0.545

**Tape and Reel Information**

<table>
<thead>
<tr>
<th>Tape</th>
<th>Embossed carrier tape</th>
</tr>
</thead>
<tbody>
<tr>
<td>Quantity</td>
<td>20000pcs</td>
</tr>
<tr>
<td>Direction of reel</td>
<td>E2</td>
</tr>
</tbody>
</table>

- **Note:** The direction is the 1pin of product is at the upper left when you hold seal on the left hand and you pull out the tape on the right hand.

**Drawing No.:** EX111-5002

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TSZ221111 - 15 - 001

15/16

TSZ02201-0F4F0C300100-1-2
<table>
<thead>
<tr>
<th>Date</th>
<th>Revision</th>
<th>Changes</th>
</tr>
</thead>
<tbody>
<tr>
<td>20. Apr. 2016</td>
<td>002</td>
<td>P2 Footprints and Traces</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Changed the representation of the units.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>(74.2\text{mm}^2) (Square) (\Rightarrow) (74.2\text{mm} \times 74.2\text{mm})</td>
</tr>
</tbody>
</table>
Notice

Precaution on using ROHM Products

1. Our Products are designed and manufactured for application in ordinary electronic equipments (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment, transport equipment, traffic equipment, aircraft/ spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property (“Specific Applications”), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM’s Products for Specific Applications.

2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
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   b. Installation of redundant circuits to reduce the impact of single or multiple circuit failure

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   a. Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
   b. Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
   c. Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
   d. Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
   e. Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
   f. Sealing or coating our Products with resin or other coating materials
   g. Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
   h. Use of the Products in places subject to dew condensation

4. The Products are not subject to radiation-proof design.

5. Please verify and confirm characteristics of the final or mounted products in using the Products.

6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse) is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.

8. Confirm that operation temperature is within the specified range described in the product specification.

9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.

2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification
Precautions Regarding Application Examples and External Circuits
1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.

2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic
This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation
1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
   [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
   [b] the temperature or humidity exceeds those recommended by ROHM
   [c] the Products are exposed to direct sunshine or condensation
   [d] the Products are exposed to high Electrostatic

2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.

3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.

4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

Precaution for Product Label
A two-dimensional barcode printed on ROHM Products label is for ROHM’s internal use only.

Precaution for Disposition
When disposing Products please dispose them properly using an authorized industry waste company.

Precaution for Foreign Exchange and Foreign Trade act
Since concerned goods might be fallen under listed items of export control prescribed by Foreign exchange and Foreign trade act, please consult with ROHM in case of export.

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